

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application. All claims currently being amended are shown with deleted text struckthrough or double bracketed and new text underlined. Additionally, the status of each claims is indicated in parenthetical expression following the claim number.

Claims 1-20 remain.

Claim 2 is cancelled.

Claims 1 and 15 are being amended.

WHAT IS CLAIMED IS:

A¹
1. (Currently Amended) A system for remotely programming a flash memory comprising:

a host system for developing code;
a wireless transmitter associated with the host system for transmitting the code;
a wireless receiver associated with the flash memory for receiving the transmitted code; and
circuitry for programming ~~storing~~ the received code in the flash memory.

2. (Cancelled)

3. (Original) The system of Claim 1 wherein the wireless transmitter comprises an RF transmitter and the wireless receiver comprises an RF receiver.

4. (Original) The system of Claim 1 wherein the wireless transmitter comprises an IR transmitter and the wireless receiver comprises an IR receiver.

5. (Original) The system of Claim 1 wherein the wireless transmitter transmits the code in a format emulating a wired connection between the host system and the circuitry for storing.
6. (Original) The system of Claim 1 wherein the circuitry for storing the received code is integrated with the memory.
7. (Original) The system of Claim 1 wherein the wireless receiver is integrated with the memory.
8. (Original) A system comprising:
a processor operating in response to code programmed into an associated Flash memory;
a wireless receiver for receiving the code from a remote development system;
and
circuitry for programming the code received by the wireless receiver into the Flash memory.
9. (Original) The system of Claim 8 wherein the processor and the flash memory are disposed on a development board.
10. (Original) The system of Claim 8 wherein the processor and the flash memory are disposed on a production board.
11. (Original) The system of Claim 8 wherein the processor forms a portion of a system-on-a-chip.
12. (Original) The system of Claim 8 wherein the circuitry for programming is integrated with the flash memory.
13. (Original) The system of Claim 11 wherein the circuitry for programming is integrated into the system-on-a-chip.

14. (Original) The system of Claim 8 wherein the flash memory is disposed in an enclosure including means for providing the code to an input of the wireless receiver.

15. (Currently Amended) A method of remotely programming a flash memory associated with a wireless receiver and circuitry for storing code received by the wireless receiver in the memory comprising the steps of:

developing the code on a simulation system;

transmitting the code via a wireless link from the simulation system to the wireless receiver associated with the flash memory; and

storing the code in the flash memory.

16. (Original) The method of Claim 15 wherein said step of transmitting comprises the substep of formatting the code into a format emulating a wired connection between the simulation system and the memory.

17. (Original) The method of Claim 16 wherein the format is selected from the group comprising the PCI, RS232, IEEE 1394, USB and I2C bus protocols.

18. (Original) The method of Claim 15 wherein said step of transmitting comprises the step of transmitting the code via an RF wireless link.

19. (Original) The method of Claim 15 wherein said step of transmitting comprises the step of transmitting the code via an IR wireless link.

20. (Original) The method of Claim 15 and further comprising the step of erasing the memory of previously stored code prior to said step of transmitting.